



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Hideharu Ozaki	<b>Examiner:</b>	James Kerveros
<b>Serial No.:</b>	10/063,472	<b>Art Unit:</b>	2133
<b>Filed:</b>	April 26, 2002	<b>Docket:</b>	15483
<b>For:</b>	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND DEVICE FOR TESTING SAME	<b>Dated:</b>	April 19, 2005

**Confirmation No. 1170**

Mail Stop Amendment  
Commissioner for Patents  
Alexandria, VA 22313-1450

**AMENDMENT AND RESPONSE**

Sir:

This Amendment is responsive to the outstanding Final Office Action dated October 20, 2004. This amendment supercedes the previous Amendment and Response Under 37 C.F.R. §1.116 filed on February 22, 2005, which was not entered. The applicant respectfully presents the following Amendment to the Claims and Remarks as set forth herein below.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450 Alexandria, VA 22313-1450 on April 19, 2005.

Dated: April 19, 2005

  
Seth Weinfeld